Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	63	(US-20030129861-\$ or US-20040000940-\$ or US-20050025190-\$ or US-20050200390-\$).did. or (US-5223755-\$ or US-5555214-\$ or US-5604775-\$ or US-5633830-\$ or US-5764080-\$ or US-5970255-\$ or US-5978281-\$ or US-6002282-\$ or US-6011744-\$ or US-604222-\$ or US-6011744-\$ or US-604222-\$ or US-6049255-\$ or US-6128692-\$ or US-6100713-\$ or US-6128692-\$ or US-6140854-\$ or US-6147520-\$ or US-6150863-\$ or US-6157690-\$ or US-6252419-\$ or US-6236231-\$ or US-6252419-\$ or US-6269051-\$). did. or (US-6279073-\$ or US-6342796-\$ or US-6346843-\$ or US-6351165-\$ or US-6351166-\$ or US-6351165-\$ or US-6342796-\$ or US-64420197-\$ or US-6442102-\$ or US-6472904-\$ or US-6466491-\$ or US-6472904-\$ or US-6492852-\$ or US-6504790-\$ or US-6509776-\$ or US-6504790-\$ or US-6509776-\$ or US-6577694-\$ or US-650901-\$ or US-6577694-\$ or US-6806733-\$ or US-6864715-\$). did. or (US-6894531-\$ or US-6806733-\$ or US-6864715-\$). did. or (US-6894531-\$ or US-6806733-\$ or US-6864715-\$). did. or (US-6894531-\$ or US-6940768-\$ or US-6946872-\$ or US-6940768-\$ or US-6946872-\$ or US-6940768-\$ or US-6946872-\$ or US-7123051-\$ or US-7126399-\$). did.	US-PGPUB; USPAT	OR	OFF	2007/01/30 16:19
L2	5	1 and ((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (reference adj (clock or signal)) and (phase adj detector) and (first or second) and (storage\$1 or latch\$3 or (flip adj flop\$1)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:30
L3	. 4	1 and ((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (reference adj (clock or signal)) and (phase adj detector) and (first or second) and (storage\$1 or latch\$3 or (flip adj flop\$1)) and counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:31

L4	4	1 and ((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (reference adj (clock or signal)) and (phase adj detector) and (control adj (bit\$1 or signal\$1)) and (storage\$1 or latch\$3 or (flip adj flop\$1)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:31
L5	2	1 and ((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (delay\$3 near6 (reference adj (clock or signal))) and (phas\$3 near2 (detect\$3 or compar\$5)) and (control adj (bit\$1 or signal\$1)) and stor\$3 and (strob\$3 adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:32
L6	49	((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (reference adj (clock or signal)) and (phase adj detector) and (first or second) and (storage\$1 or latch\$3 or (flip adj flop\$1)) and counter).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:43
L7	98	((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (reference adj (clock or signal)) and (phase adj detector) and (control adj (bit\$1 or signal\$1)) and (storage\$1 or latch\$3 or (flip adj flop\$1))).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2007/01/30 16:31
L8.	5	((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (delay\$3 near6 (reference adj (clock or signal))) and (phas\$3 near2 (detect\$3 or compar\$5)) and (control adj (bit\$1 or signal\$1)) and stor\$3 and (strob\$3 adj signal)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF .	2007/01/30 16:32
L9	1	1 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:32
L10	1	1 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:32

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L11	0	1 and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:32
L12	0	6 and 713/400.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:34
L13	. 1	6 and 713/401.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:33
L14	1	6 and 713/503.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:33
L15	0	7 and 713/400.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF ·	2007/01/30 16:35
L16	3	7 and 713/401.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:34
L17	2	7 and 713/503.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:34
L18	0	8 and 713/400.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:35
L19	0	8 and 713/401.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:35
L20	0	8 and 713/503.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ,	OFF	2007/01/30 16:35

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L21	3	(CHONG adj YAN).in.	US-PGPUB	OR	OFF	2007/01/30 16:39
L22	14	(SUNG adj CHIAKANG).in.	US-PGPUB	OR	OFF	2007/01/30 16:40
·L23	29	(HUANG adj JOSEPH).in.	US-PGPUB	OR	OFF	2007/01/30 16:40
L24	5	(PAN adj PHILIP).in.	US-PGPUB	OR	OFF	2007/01/30 16:40
L25	0	(21 or 22 or 23 or 24) and 6	US-PGPUB	OR	OFF	2007/01/30 16:39
L26	0	(21 or 22 or 23 or 24) and 7	US-PGPUB	OR	OFF	2007/01/30 16:39
L27	0	(21 or 22 or 23 or 24) and 8	US-PGPUB	OR	OFF	2007/01/30 16:39
L28	31	(CHONG adj YAN).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2007/01/30 16:39
L29	. 130	(SUNG adj CHIAKANG).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:40
L30	165	(HUANG adj JOSEPH).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:40
L31	29	(PAN adj PHILIP).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF .	2007/01/30 16:40
L32	1	(28 or 29 or 30 or 31) and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:41
L33	1	(28 or 29 or 30 or 31) and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:41
L34	0	(28 or 29 or 30 or 31) and 8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/30 16:41

INTERFERENCE SEARCH

L35	1	(((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) near6 (reference adj (clock or signal))) and ((phase adj detector) near6 counter) and (storage\$1 or latch\$3 or (flip adj flop\$1)) and control).clm.	US-PGPUB	OR	OFF	2007/01/30 16:51
L36	1	((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and ((control adj (circuit or device or module or unit)) near6 (reference adj (clock or signal))) and (storage\$1 near6 (control adj (bit\$1 or signal\$1))) and clock\$3). clm.	US-PĢPUB	OR	OFF	2007/01/30 16:55
L37	. 1	((delay adj (element\$1 or cell\$1 or circuit\$1 or line\$1 or module\$1 or unit\$1)) and (reference adj (clock or signal)) and (control adj (bit\$1 or signal\$1)) and (delay\$3 near6 (data near2 strob\$3)) and compar\$5 and stor\$3).clm.	US-PGPUB	OR	OFF	2007/01/30 16:59